

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stefano Sivero et al.

PATENT APPLICATION

Serial No.: 10/666,142

Group Art Unit: 2631

Filed: September 17, 2003

Examiner:

For: REGENERATIVE CLOCK REPEATER

## Supplemental Information Disclosure Statement

Hon. Commissioner for Patents Alexandria, VA 22313

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.56. A copy of the reference is enclosed.

## Other Reference

H.B. Bakoglu, "Circuit, Interconnections, and Packaging for VLSI", Addison-Wesley Publishing Company, Inc., 1990, pages 198-219.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail envelope addressed Commissioner for Patents, Alexandria, VA 22313

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